

14/04/03

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L1 and (thermal adj catalysis)

2

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<u>L3</u>	L1 and (thermal adj catalysis)	2	<u>L3</u>
<u>L2</u>	L1 (interfacial near2 density)	1	<u>L2</u>
<u>L1</u>	semiconductor near insulator	4894	<u>L1</u>

END OF SEARCH HISTORY

*fill Patent
from search application*

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1. Document ID: US 6349669 B1

L3: Entry 1 of 2

File: USPT

Feb 26, 2002

US-PAT-NO: 6349669

DOCUMENT-IDENTIFIER: US 6349669 B1

TITLE: Method and apparatus for depositing a thin film, and semiconductor device having a semiconductor-insulator junction

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC
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2. Document ID: US 6069094 A

L3: Entry 2 of 2

File: USPT

May 30, 2000

US-PAT-NO: 6069094

DOCUMENT-IDENTIFIER: US 6069094 A

TITLE: Method for depositing a thin film

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC
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1. Document ID: US 5254488 A

L2: Entry 1 of 1

File: USPT

Oct 19, 1993

US-PAT-NO: 5254488

DOCUMENT-IDENTIFIER: US 5254488 A

TITLE: Easily manufacturable thin film transistor structures

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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L1 (interfacial near2 density)	1

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